

# An Overview of SWIPT Circuits and Systems



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**Abstract:** From a circuit implementation perspective, this paper presents a brief overview of simultaneous wireless information and power transmission (SWIPT). By using zero-power batteryless wireless sensors, SWIPT mixes wireless power transmission with wireless communications to allow the truly practical implementation of the Internet of Things as well as many other applications. In this paper, technical backgrounds, problem formation, state-of-the-art solutions, circuit implementation examples, and system integrations of SWIPT are presented.

**Keywords:** simultaneous wireless information and power transmission (SWIPT) systems; backscatter communication; low power communication

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## 1 Introduction

In addition to radio frequency (RF) waves' ability to transmit information, a viable energy source for powering low-power devices used in wireless sensor networks (WSN) or Internet of Things (IoT) applications should also be considered. The scientific community has explored this characteristic of radio waves, resulting in the proposal of a simultaneous wireless information and power transmission (SWIPT) system.

A careful design that incorporates wireless power transfer (WPT) capabilities should be considered to improve the devices' data storage or increase computing capabilities. This integration has facilitated the interest in the concept of passive wireless sensors, which may play an important role in an IoT scenario with sensors without batteries deployed everywhere sensing the environment. Nowadays, more IoT sensors bring the increase of batteries to be deployed, which will have a negative ambient impact. Battery-powered tags can improve communication distance but have limitations in

battery cost and replacement. Thus, the alternatives to the battery systems are based on the energy harvester (EH) technology or other different sources (solar<sup>[1]</sup>, motion or vibration<sup>[2]</sup>, and ambient RF<sup>[3]</sup>). To overcome the problems from the EH and batteries, the concept of WPT has been explored to supply the tags with power.

In Ref. [4], a solution combining inductive WPT and ultra-high frequency (UHF) radio frequency identification (RFID) was presented, and by combining these two technologies it was possible to increase the tag sensitivity by 21 dB. However, with inductive WPT, the proximity of the tag to the power source is a huge obstacle to those systems, and to overcome the problem created by the short distance required in inductive wireless power transfer, electromagnetic wireless power transfer has emerged. It consists in using the power contained in RF waves and, through conditioning circuits such as RF to (direct current) DC converters, transforming this power into the current.

Regarding electromagnetic WPT, a comparison between different rectifier typologies and different stage levels was presented in Ref. [5]. The results show a high dependence between the received power and the most efficient topology. In Ref. [6], the authors presented a structure with a two-tone

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signal at 1.8 GHz and 2.4 GHz, which improved the voltage output by 20% higher on average than a single-tone input. The authors in Ref. [7] presented a reader which was configured to transmit power in continuous waves (CW). The tag used a storage capacitor that could charge up to 5.5 V after the rectification. After the storage capacitor was charged, it powered the tag that performed sensing and communication, reflecting the carrier wave. This process was proven at 1 m between the reader and the tag. The same principle was used in Ref. [8].

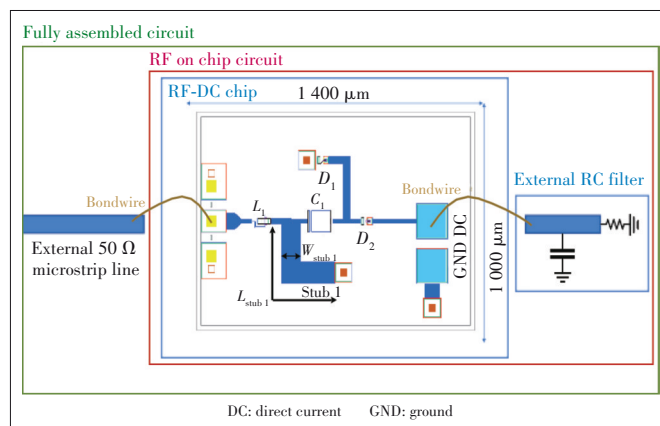
This paper describes the works presenting SWIPT systems or devices that are part of SWIPT systems, power conditioning circuits such as RF-DC converters, or backscatter communication circuits.

This paper is organized as follows. Section 2 presents some examples of work in the WPT area, Section 3 exposes backscatter communication systems, SWIPT systems are presented in Section 4, and the conclusions are drawn in Section 5.

## 2 Wireless Power Transfer Energy Harvesters

As mentioned previously, the increase of IoT devices raises the need for power and EH is a clean solution to that. Moreover, with new 5G bands (above 20 GHz), new opportunities emerge, due to the possibility of developing compact devices, using higher transmitted power, and using antenna arrays instead of single elements. Integrated circuits (IC) are also a solution to developing compact and quite cheap circuits, and Gallium Arsenide (GaAs) technology has evolved in recent years, making it a suitable technology for low-power systems.

In Ref. [9] the authors presented a hybrid solution combining an RF-DC converter chip, based on a 2-stage Dickson charge pump/voltage multiplier that was developed by resorting to a 100  $\mu\text{m}$  GaAs technology foundry. In contrast to conventional circuits developed with IC technologies, the authors in Ref. [9] developed a DC low-pass filter externally to the chip, using a conventional microstrip substrate connected by bond wires, as shown in Fig. 1. The chip is composed of a matching network (values are shown in Fig. 1) that matches the input impedance to the diode's impedance. The external RC filter will allow tuning the circuit for different scenarios and frequencies. For example, when measuring the chip using a probe station, the RC network is not the same if the circuit is connected to an antenna, since the 50  $\Omega$  microstrip line and bond-wire would influence the chip response. This enables optimization of the circuit for different scenarios and different frequencies of operation. The substrate used is the Rogers RO4003C, with  $\epsilon_r=3.48$  and  $h=0.508$  mm. With this circuit, for the case of on-chip measurement, 51 % efficiency is achieved for an input power of 15 dBm. In the fully assembled prototype, some de-



▲ Figure 1. RF-DC block diagram circuit for different scenarios, where  $L_1=0.12$  nH,  $C_1=2.7$  pF,  $L_{\text{stub}1}=372$   $\mu\text{m}$ ,  $W_{\text{stub}1}=90$   $\mu\text{m}$ ,  $W_{D1}=W_{D2}=10$  (N° of fingers), with the chip area of 1 400  $\mu\text{m} \times 1$  000  $\mu\text{m}$ <sup>[9]</sup>

viation is found, and the peak of efficiency appears at 23 GHz with efficiency of 54% and the input power of 13 dBm. Moreover, both circuits present promising results at other frequencies.

Using this approach, it is possible to power up a sensor even without the use of a battery pack, allowing the possibility of maximizing the service time of IoT sensors.

## 3 Wireless Information

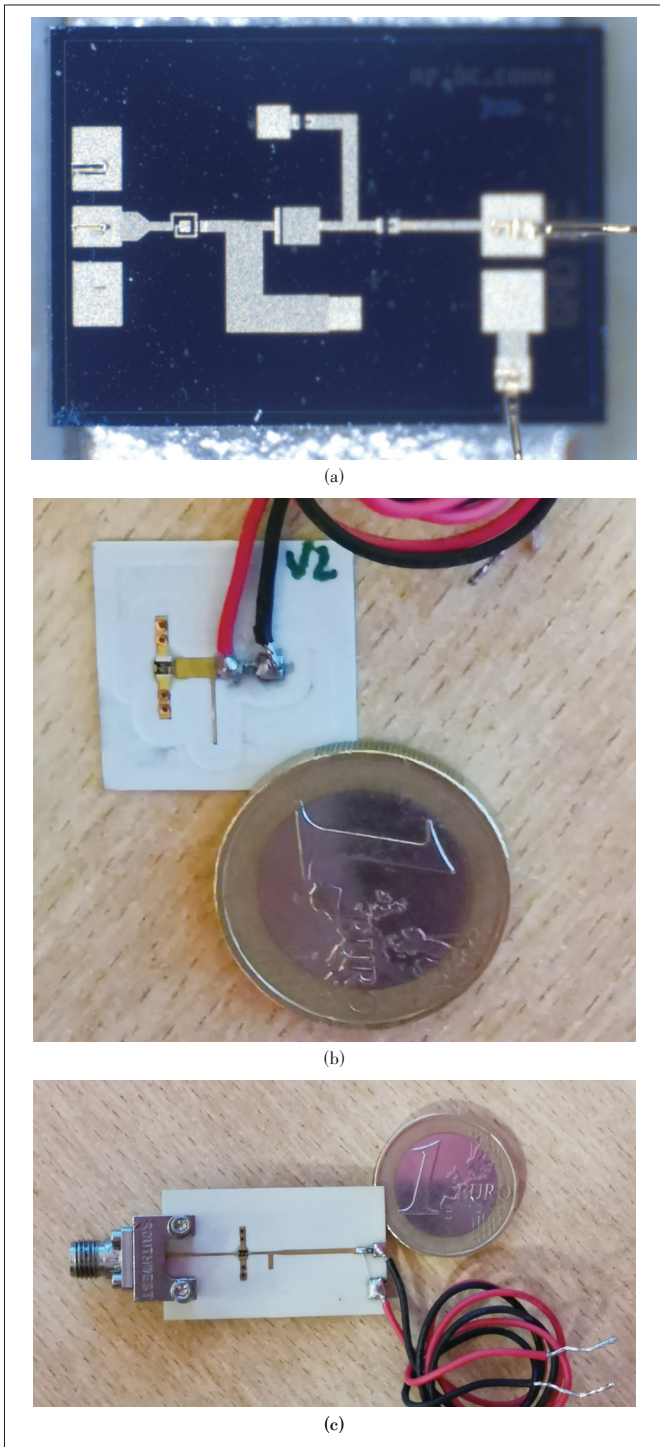
### 3.1 High Order Backscatter Communication

If the objective is to create an information layer on top of the power layer for the IoT sensors, the wireless communication layer should consume the minimum amount of energy possible. Traditional super-heterodyne approaches are not a viable solution in this scenario, since the energy cost is high for these approaches. One of the alternatives proposed by the authors in Ref. [10] is to use backscatter systems combined with WPT.

Backscatter systems are normally low bit-rate solutions, because they operate in amplitude shift keying, or binary phase shift keying modulation formats, creating different impedances at the antenna plane for the generation of such modulation schemes. Nevertheless, it is possible to combine some basic modulations to obtain better modulation formats for IoT applications, like the quadrature amplitude modulation (QAM) which combines the amplitude and phase modulation.

This technique uses the combination to change the antenna impedance, and each impedance will correspond to a symbol used to transmit the digital message. DASKALAKIS et al.<sup>[11]</sup> presented a low-power sensor tag. The tag could create up to 4 different antenna impedances, modulate data using four-pulse amplitude modulation (PAM) to increase the transmitted bit rate, and send it to a low-cost software-defined radio (SDR) reader. The tag requires no batteries and is supplied with an energy harvester, consuming only 27  $\mu\text{W}$ .

In Ref. [12], the authors presented a novel modulator that



▲ Figure 2. (a) Microscopic photograph of the chip, (b) on-chip scenario prototype, and (c) fully assembled circuit prototype<sup>[9]</sup>

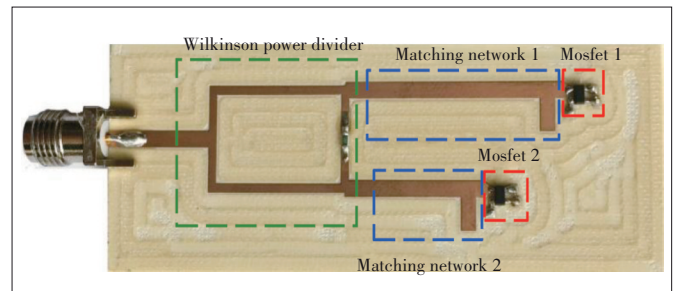
worked with a Wilkinson power divider with a phase shift and two transistors working as switches to generate M-QAM, as can be seen in Fig. 3. This modulation technique permits high-bandwidth and low-power wireless communications. The presented circuit has an energy consumption as low as 6.7 pJ/bit for a bit rate of 120 Mbit/s.

In Ref. [13], the authors implemented the same approach presented previously, but in this case, they used a 130 nm BiCMOS foundry technology. The objective was to achieve a low-power and high-speed M-QAM backscatter modulator for millimeter wave frequencies, as shown in Fig. 4. The authors showed that the modulator presented the lowest dynamic power consumption reported at the time, which was 6.96  $\mu$ W, and could achieve 16, 32, and 64-QAM modulations in a range of 20 GHz to 28 GHz with a low value of error vector magnitude (EVM).

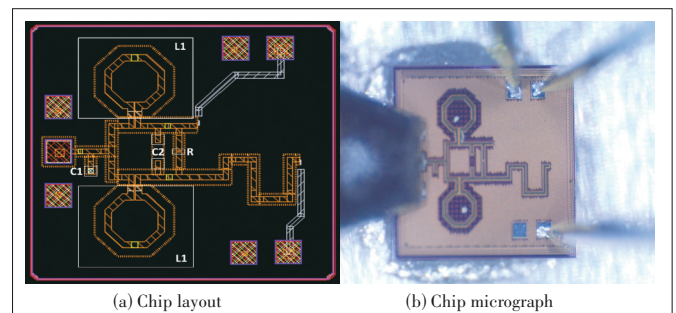
Moreover, backscatter could also be used with ambient electromagnetic waves, as in Ref. [14], which was based on the circuit of Ref. [13] and was possible to perform PAM modulation. In this case, the authors were interested in signal bands that are usually found on our daily basis, such as frequency modulation (FM)-100 MHz, industrial, scientific and medical (ISM)-400 MHz, digital video broadcasting-terrestrial (DVB-T)-700 MHz, or global system for mobile communications (GSM)-900 MHz. To demodulate the received signal, the authors have used a low-cost SDR that covers the bands of interest. The authors demonstrated that their system could demodulate signals in an extensive range of input power (-10 dBm to -60 dBm) and several data rate values (5 kbit/s to 50 kbit/s) in the mentioned frequencies.

### 3.2 All Digital Backscatter Communication

To minimize the costs of producing backscatter tags and increase the microcontroller unit (MCU) capability to receive and store data from the sensor networks, TORRES et



▲ Figure 3. Photograph of the 16-quadrature amplitude modulation (QAM) backscatter circuit<sup>[12]</sup>



▲ Figure 4. BiCMOS high-order backscatter modulator, in which  $C_1 = 385.117$  fF,  $L_1 = 0.3$  nH,  $C_2 = 30.012$  fF,  $R = 100$   $\Omega$ , with the chip area of  $0.92$  mm<sup>2</sup><sup>[13]</sup>



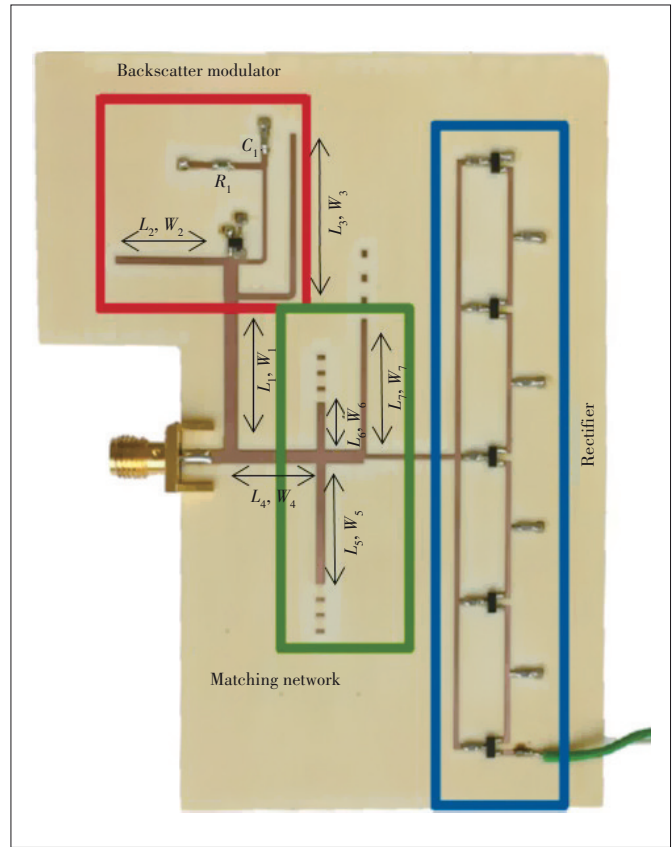
al.<sup>[15]</sup> showed a fully digital ambient backscatter system that operates with ambient FM sources, with a power of approximately  $-70$  dBm. In this system, the backscatter module uses an ESP32 MCU and a telescopic monopole as an antenna. The variation of a digital I/O pin causes an impedance variation in the connected antenna reflecting the incoming FM waves with an amplitude shift keying (ASK) modulation and can be demodulated by a low-cost RTL-SDR USB dongle, as shown in Fig. 5.

The solution presented enables a large number of applications that can benefit from the different technologies used with the commercial modules tested, such as Wi-Fi, Bluetooth, and ambient backscatter communications.

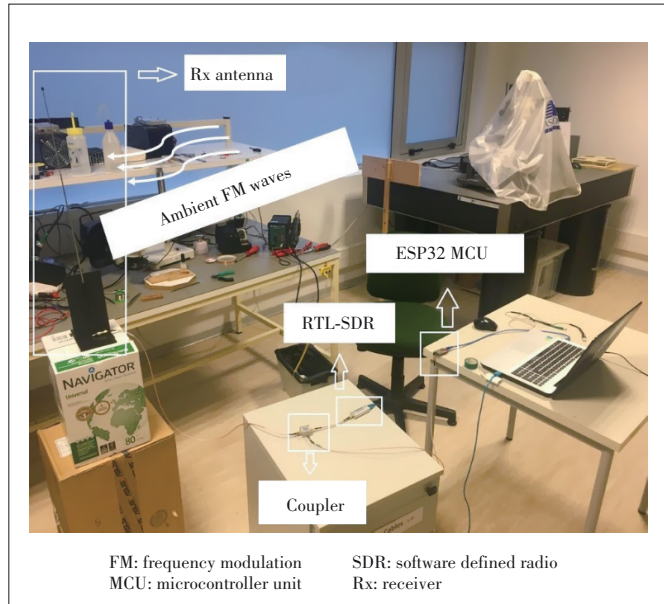
In Ref. [16], the authors produced a digital backscatter tag connecting a dipole, the two red wires in Fig. 6, to a digital I/O pin of the PIC16 MCU, presenting a digital backscatter tag.

### 4 SWIPT System

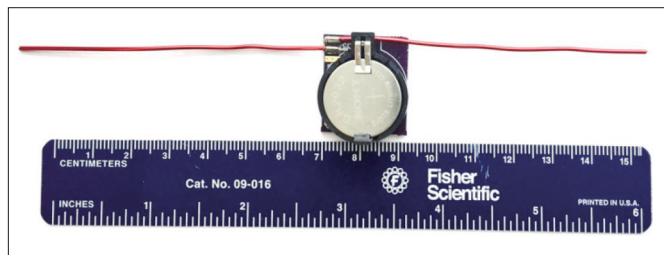
The authors in Refs. [10] and [17], as shown in Fig. 7, presented a solution using dual-band wireless power and data transfer, a system with a backscatter modulator combined with WPT. In this solution, two frequencies were consid-



▲ Figure 7. Photograph of implemented system with backscatter modulator combined with wireless power transfer (WPT), element values of which are  $L_1 = 21.2$  mm,  $W_1 = 1.87$  mm,  $L_2 = 15.1$  mm,  $W_2 = 1.0$  mm,  $L_3 = 21.9$  mm,  $W_3 = 0.8$  mm,  $L_4 = 11.3$  mm,  $W_4 = 1.87$  mm,  $L_5 = 17.1$  mm,  $W_5 = 1.2$  mm, and  $L_6 = 6.7$  mm<sup>[17]</sup>



▲ Figure 5. Real ambient FM measurements setup<sup>[15]</sup>



▲ Figure 6. Photo of digital backscatter prototype<sup>[16]</sup>

ered: one was used to power the tag and the other to perform the communication through backscatter. In this circuit, it is possible to find three main blocks. In the communications part, the red square in Fig. 7, the backscatter modulator will vary the impedance seen by the antenna, causing a reflection or absorption of the incident wave. The matching network is presented in green in Fig. 7, which is designed to provide backscatter load modulation at one frequency and continuous flow of WPT at other frequencies, and the five-stage Dickson multiplier provides sufficient DC power to supply the micro-controller. A similar approach, using different frequencies for energy and communication, was presented in Ref. [18], in which a different circuit for the RF-DC conversion was used for each frequency. The authors in Ref. [19] used two different frequencies for RF communication and WPT. They also used 5.8 GHz to power up a portion of radio connected to a battery with an RF-DC converter, and a wake-on radio (WOR) will only activate the battery for the primary transceiver. Nevertheless, the system is not passive and uses the WPT to activate the primary transceiver. Despite having passive tags for communication, it is of utmost importance to improve the data rate in the sensors in

order to reduce the power consumption and extend the read range.

## 5 Conclusions

In this paper, a brief review of SWIPT systems is presented, especially the combination of wireless power transmission and backscatter communications. The use of higher order modulation formats allows the implementation of such systems, reducing significantly the need for batteries in these sensors and paving the path for Internet of Everything in the future.

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